

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	541	703/14.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L2	335	703/13.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L3	103	703/17.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L4	66	703/20.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L5	3	((("6052524") or ("6263302") or ("6604067"))).PN.	US-PGPUB; USPAT	OR	OFF	2006/01/06 15:03
L6	64517	memory near4 configur\$6	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L7	38219	(embed\$4 asic) and simulat\$4	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L8	3164	L6 and L7	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L9	2850	L8 and access	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L10	1936	L9 and space	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L11	1449	L10 and read and write	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L12	423	L11 and statistic\$3	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L13	405	L12 and design	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L14	366	L13 and (criteria specification)	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L15	345	L14 and program	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L16	253	L15 and usage	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L17	109	L16 and @ad<="20000609"	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03

		Results
19.	(pub-date > 1959 and pub-date < 2001 and TITLE-ABSTR-KEY(memory) and TITLE-ABSTR-KEY(design)) and (embed or asic) [All Sources(- All Sciences -)]	57
18.	pub-date > 1959 and pub-date < 2001 and TITLE-ABSTR-KEY(memory) and TITLE-ABSTR-KEY(design) [All Sources(- All Sciences -)]	1545
17.	pub-date > 1959 and pub-date < 2001 and TITLE-ABSTR-KEY(memory) and TITLE-ABSTR-KEY((embed or asic)) [All Sources(- All Sciences -)]	21
16.	pub-date > 1959 and pub-date < 2001 and TITLE-ABSTR-KEY(memory configuration) [All Sources(- All Sciences -)]	5
15.	((((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space) and design) and usage) and size [All Sources(- All Sciences -)]	95
14.	((((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space) and design) and usage) and size [All Sources(- All Sciences -)]	95
13.	((((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space) and design) and usage [All Sources(- All Sciences -)]	100
12.	((((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space) and design [All Sources(- All Sciences -)]	210
11.	((((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space [All Sources(- All Sciences -)]	221
10.	((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access [All Sources(- All Sciences -)]	244
9.	(((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write [All Sources(- All Sciences -)]	327
8.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read [All Sources(- All Sciences -)]	535
7.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification) [All Sources(- All Sciences -)]	1028
6.	((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic! [All Sources(- All Sciences -)]	1412
5.	(pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration [All Sources(- All Sciences -)]	2799
4.	pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!) [All Sources(- All Sciences -)]	6428
3.	(((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT(memory configuration)) and statistic!) and simulat!) and access) and usage [All Sources(- All Sciences -)]	3
2.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT(memory configuration)) and statistic!) and simulat!) and access [All Sources(- All Sciences -)]	26
1.	((pub-date > 1959 and pub-date < 2001 and FULL-TEXT(memory configuration)) and statistic!) and simulat! [All Sources(- All Sciences -)]	38

[Search Session History](#)[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Edit an existing query or
compose a new query in the
Search Query Display.

Fri, 6 Jan 2006, 6:04:23 PM EST

Search Query Display

Select a search number (#)
to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

		Results
<u>#1</u>	((memory design<in>metadata) <and> (pyr >= 1951 <and> pyr <= 2000)	160
<u>#2</u>	(((memory design<in>metadata) <and> (on-chip<or>embed<or>asic<in>metadata)) <and> (pyr >= 1951 <and> pyr <= 2000)	33
<u>#3</u>	(((memory configur*<in>metadata) <and> (on-chip<or>embed<or>asic<in>metadata)) <and> (pyr >= 1951 <and> pyr <= 2000)	16

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2005 IEEE – All Rights Reserved




[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide

THE ACM DIGITAL LIBRARY


[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Published before July 2000

Terms used [memory configuration](#) [memory design](#) [embed asic](#) [on chip](#)

Found 17 of 110,893

Sort results by
☒ Save results to a Binder
Try an [Advanced Search](#)Try this search in [The ACM Guide](#)Display results
☒ Search Tips

☐ Open results in a new window

Results 1 - 17 of 17

Relevance scale ☐ ☐ ☐ ☐ ☐1 [High-level library mapping for memories](#)Pradip K. Jha, Nikil D. Dutt
July 2000**ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 3

Publisher: ACM Press

Full text available: pdf(209.38 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)2 [Trap-driven simulation with Tapeworm II](#)Richard Uhlig, David Nagle, Trevor Mudge, Stuart Sechrest
November 1994**ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the sixth international conference on Architectural support for programming languages and operating systems ASPLOS-VI**, Volume 29 , 28 Issue 11 , 5

Publisher: ACM Press

Full text available: pdf(1.45 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)3 [A characterization of ten rasterization techniques](#)N. Gharachorloo, S. Gupta, R. F. Sproull, I. E. Sutherland
July 1989**ACM SIGGRAPH Computer Graphics , Proceedings of the 16th annual conference on Computer graphics and interactive techniques SIGGRAPH '89**, Volume 23 Issue 3

Publisher: ACM Press

Full text available: pdf(5.15 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)4 [Accommodating memory latency in a low-cost rasterizer](#)Bruce Anderson, Andy Stewart, Rob MacAulay, Turner Whitted
August 1997**Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**

Publisher: ACM Press

Full text available: pdf(585.61 KB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)5 [Trace-driven memory simulation: a survey](#)

Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Publisher: ACM Press

Full text available: pdf(636.11 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#),
[review](#)6 [Memory modeling for system synthesis](#)Sari L. Coumeri, Donald E. Thomas
August 1998**Proceedings of the 1998 international symposium on Low power electronics and design**

Publisher: ACM Press



















Full text available: pdf(810.71 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)7 [Evaluating the memory overhead required for COMA architectures](#)T. Joe, J. L. Hennessy
April 1994**ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture ISCA '94**, Volume 22 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.31 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)8 [Introducing memory into the switch elements of multiprocessor interconnection networks](#)

-  H. E. Mizrahi, J. L. Baer, E. D. Lazowska, J. Zahorjan
April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89**, Volume 17 Issue 3
Publisher: ACM Press
Full text available:  pdf(1.15 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
- 9 Co-design of interleaved memory systems
 Hua Lin, Wayne Wolf
May 2000 **Proceedings of the eighth international workshop on Hardware/software codesign**
Publisher: ACM Press
Full text available:  pdf(91.44 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
- 10 Architecture and implementation of a VLIW supercomputer
Robert P. Colwell, W. Eric Hall, Chandra S. Joshi, David B. Papworth, Paul K. Rodman, James E. Tornes
November 1990 **Proceedings of the 1990 ACM/IEEE conference on Supercomputing**
Publisher: IEEE Computer Society
Full text available:  pdf(1.29 MB) Additional Information: [full citation](#), [abstract](#), [references](#)
- 11 Energy recovery for the design of high-speed, low-power static RAMs
N. Tzartzanis, W. Athas
August 1996 **Proceedings of the 1996 international symposium on Low power electronics and design**
Publisher: IEEE Press
Full text available:  pdf(62.15 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
- 12 Preprototyping SIMD coprocessors using virtual machine emulation and trace compilation
 Martin C. Herboldt, Owais Kidwai, Charles C. Weems
June 1997 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1997 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '97**, Volume 25 Issue 1
Publisher: ACM Press
Full text available:  pdf(2.05 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
- 13 Mirage: a coherent distributed shared memory design
 B. Fleisch, G. Popek
November 1989 **ACM SIGOPS Operating Systems Review , Proceedings of the twelfth ACM symposium on Operating systems principles SOSP '89**, Volume 23 Issue 5
Publisher: ACM Press
Full text available:  pdf(1.63 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
- 14 Emulation of computer networks by microprogrammable microcomputers
 David Cohen, Ming T. Liu
September 1974 **Conference record of the 7th annual workshop on Microprogramming**
Publisher: ACM Press
Full text available:  pdf(430.27 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
- 15 Exploiting parallelism in a Relational Associative Processor
 Paul J. Sadowski, S. A. Schuster
August 1978 **ACM SIGMOD Record , ACM SIGIR Forum , ACM SIGARCH Computer Architecture News**, Volume 10 , 13 , 7 Issue 1 , 2 , 2
Publisher: ACM Press
Full text available:  pdf(1.02 MB) Additional Information: [full citation](#), [abstract](#), [references](#)
- 16 Application-controlled physical memory using external page-cache management
 Kieran Harty, David R. Cheriton
September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V**, Volume 27 Issue 9
Publisher: ACM Press
Full text available:  pdf(1.40 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
- 17 A multiple stream microprocessor prototype system: AMP-1
 Edward S. Davidson
May 1980 **Proceedings of the 7th annual symposium on Computer Architecture**
Publisher: ACM Press
Full text available:  pdf(698.01 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc.
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)